Docket No.: 61282-039 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Masaya SUMITA, et al. : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: October 21, 2003 : Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS

CLAIM OF PRIORITY

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 35 U.S.C. 119, Applicants hereby claim the priority of:

Japanese Patent Application No. 2002-306138, filed October 21, 2002 and
Japanese Patent Application No. 2003-358891, filed October 20, 2003

Certified copies will be filed in due course.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogaty Registration No. 36,139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:prg Facsimile: (202) 756-8087

Date: October 21, 2003

WDC99 829704-1.061282.0039